

ABSTRACT OF THE DISCLOSURE

A test bench generation technique is described in which a simulation of test operation is performed using a full subsystem circuit model 2 and full surrounding circuit models 4, 6, and 8. The input and output signals are recorded as a print on change file 10. This print on change file is used in combination with a classification of whether signals are inputs, outputs or bidirectional and associated rules for outputs and bidirectional signals when outputs to establish a reduced model concentrating on the interaction between the subsystem circuit under test and the surrounding circuit elements. This reduced model 34 replays input data and applies rules to output data rather than actually modelling the full behaviour of the surrounding circuits. The rules associated with output signals can involve time windows, strobing relationships, settling times and settled times amongst other characteristics.

[Figure 3.]